

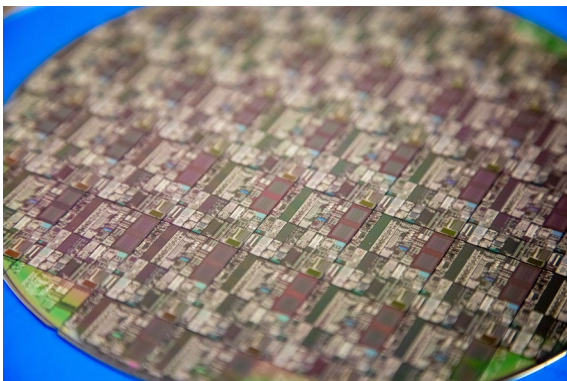


# FAB: Industrial SponsorChip

CMC Microsystems is pleased to offer the SponsorChip program for companies to enhance their research efforts and their links to academic researchers. Under this program companies may choose a chip fabrication technology and topic areas of interest and CMC takes care of the rest.

## What you Get

- ✓ Industry Sponsor: stimulate the research community and contribute to grad student skill development in your area of interest, identify potential future partners, explore topics in the design and manufacturing space, submit your own designs alongside those of the students, and benefit from CMC know-how
- ✓ Professor: low cost access to silicon that allows you, your students, and your research staff to test out new ideas and to publish
- ✓ Student: mentoring, knowledge gained in hot industrial topics, affordable chip fabrication, exposure to possible future employers



## Sponsorship Example

TSMC 65 nm CMOS Standard Process  
CAD \$80,000  
12 mm<sup>2</sup> design area  
Average 6-12 design submissions  
Technical and logistical support from CMC

Other example options:

- 130 nm CMOS from TSMC, research area: low power electronics for IoT
- 65 nm CMOS from TSMC, research area: CMOS RF circuits
- Silicon photonics with active components from AMF
- MIDIS inertial sensor process from Teledyne DALSA

CMC provides:

- A call for proposals to over 1,000 researchers and their teams
- Full suite of CAD software to researchers undertaking the sponsored activity
- Hands-on training and support of from schematic to layout to package and test
- A timeline for design submission, fabrication, and test
- Reviews of the sponsored activity to ensure quality and return on investment
- Testing support through equipment loans and process debug

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