

**Title:** Deep Learning in EDA

**Speaker:** Dr. Yu Huang (HiSilicon Co.)

**Abstract**

In the past a few years, deep-learning has been so successfully applied in several areas. Its application in EDA is still under development, but we have seen some achievements in a few areas. In this talk we will review some published work in this area, share some experiences and discuss what are the challenges ahead of us in applying DL in EDA.

**Bio**

Dr. Yu Huang is the EDA chief architect of HiSilicon Co. His research interests include VLSI SoC testing, ATPG, compression, diagnosis, machine learning and AI chips. He has a PhD in electrical and computer engineering from the University of Iowa, USA. He holds 27 US patents and has 15 more patents pending. He has published more than 120 papers on leading IEEE Journals, conferences and workshops. He is a senior member of the IEEE. He has served as technical program committee member for DAC, ITC, VTS, ATS, ETS, ASPDAC, NATW and many other conferences and workshops in the testing area. He is now serving DAC 2021 as the co-chair of Test and Reliability Track.