

## Syllabus

### Day 1:

- How to Start L-Edit
- L-Edit UI
- Customizing your environment
- Grids, Precision
- Layers and Masks
- Drawing/Editing
- Cells and Hierarchy
- Instancing Tcells
- How to start a new design
- How to attach to a technology reference
- How to work with libraries
- How to work with design kits
- How to do Boolean operations on objects
- Base points
- Object Snapping
- Alignment Functions
- Tips, Useful commands
- Find
- Rulers
- Curves
- Wires
- Helpful Macros
- Cell Operations

### Day 2:

- JFET Technology Review
- The JFET PDK
- Loading the PDK Technology

JFET Design layers  
GDS II Stream out  
GDS II Stream in  
GDS In/Out with JFET Design Kit  
JFET DRC Rules  
Running DRC  
Fixing DRC errors  
Using the PDK Library  
PDK Cells  
Instancing the PDK TCells  
The Example Design  
Best Design practices  
3D modeling and links to COMSOL  
PDK Reference documentation  
Run Overview from CMC  
Q and A